Application Data Sheet

Application Information

Subject Matter::

Application Number:: 10/632,222
Filing Date:: 07/31/03
Application Type:: Regular

Title:: Processor With Pre-Decode Logic

Utility

That Detects A Prefix In An Instruction

And Causes A Program Counter To Be Skipped

Attorney Docket Number:: TI-35452 (1962-05416)

Request for Early Publication?:: No
Request for Non-Publication?:: No
Suggested Drawing Figure:: 4
Total Drawing Sheets:: 5
Small Entity?:: No

Applicant Information

Applicant Authority type:: Inventor
Primary Citizenship Country:: France

Status:: Full Capacity

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Status:: Full Capacity

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Applicant Authority type:: Inventor Primary Citizenship Country:: Finland

Status:: Full Capacity

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Correspondence Information

Correspondence Customer Number:: 23494

Representative Information

Representative Customer Number:: 23494

DOMESTIC PRIORITY INFORMATION

Application::	Continuity	Parent	Parent Filing
	Type::	Application::	Date::
This	Provisional of	60/400,391	07/31/02
Application			

FOREIGN PRIORITY INFORMATION

Country::	Application	Filing Date::	Priority
	number::		Claimed::
EPO	03291920.1	July 30, 2003	YES

Assignee Information

Assignee name:: Texas Instruments Incorporated

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